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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. .... 08/917,003  
Priority Filing Date .... August 20, 1997  
Inventor .... Kie Y. Ahn  
Assignee .... Micron Technology, Inc.  
Priority Group Art Unit .... 2823  
Priority Examiner .... K. Eaton  
Attorney's Docket No. .... MI22-1738  
TITLE: Conductive Lines, Coaxial Lines, Integrated Circuitry, and Methods of Forming  
Conductive Lines, Coaxial Lines, and Integrated Circuitry

Assistant Commissioner for Patents  
Washington, D. C. 20231  
Attention: Official Draftsman

**SUBSTITUTE DRAWING REQUEST**

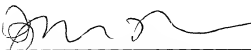
Please enter the enclosed substitute formal drawings in the above-referenced application in place of drawings originally filed. The content of the drawings are identical to those now on file in this application.

Acknowledgment of receipt of the formal drawings and their acceptance into the file is requested.

Respectfully submitted,

Date: June 24, 2001

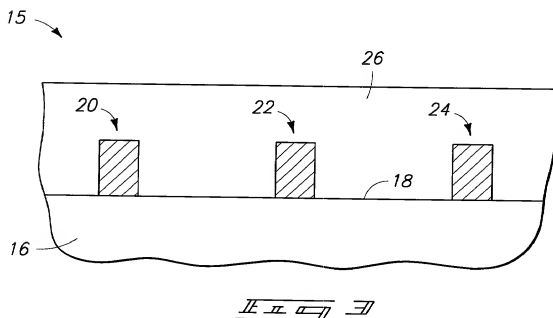
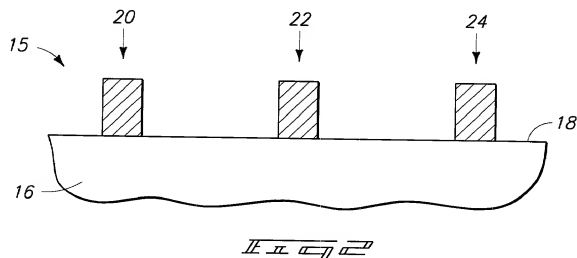
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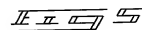
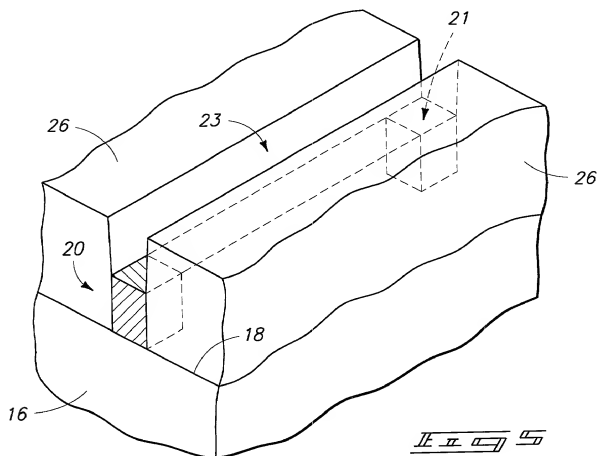
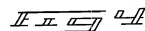
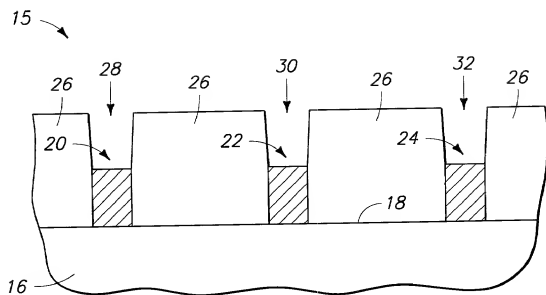
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Enclosures: 6 Sheets of Formal Drawings, Figs. 1-13.

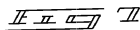
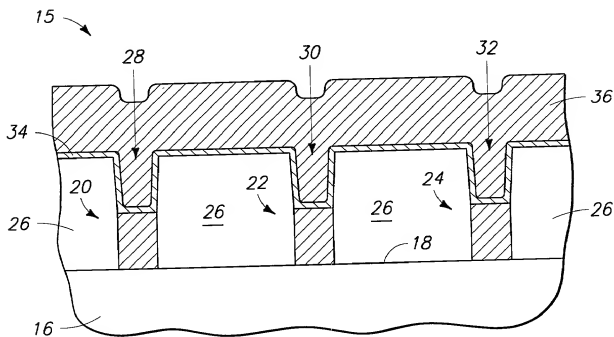
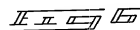
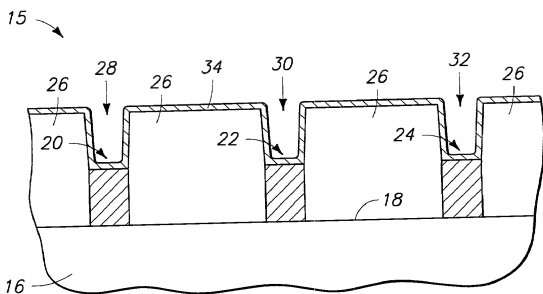
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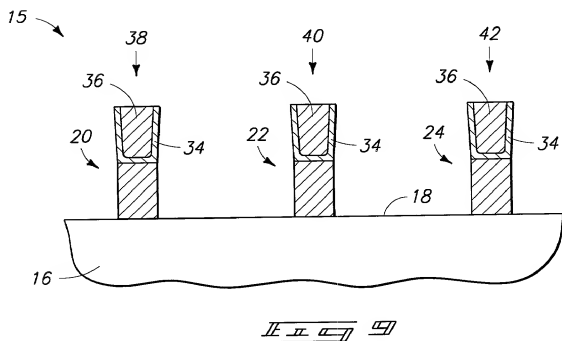
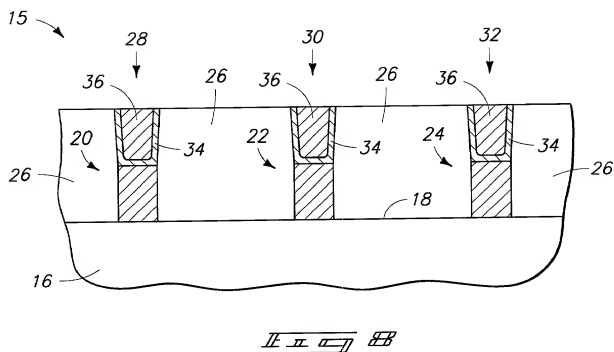


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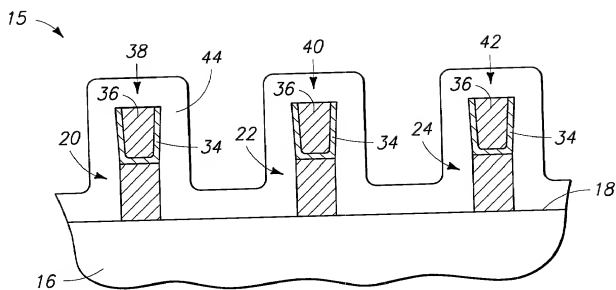
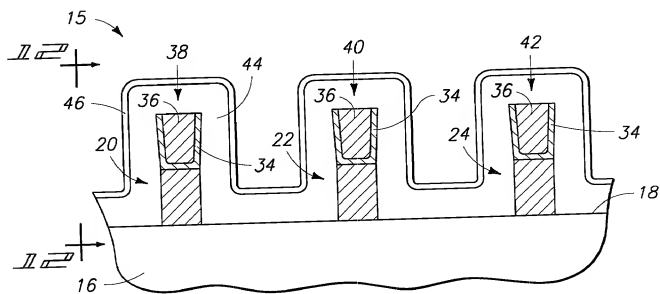


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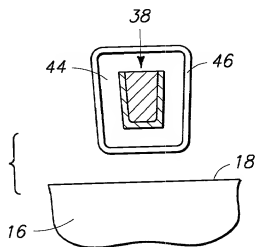
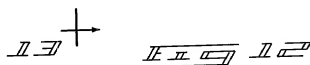
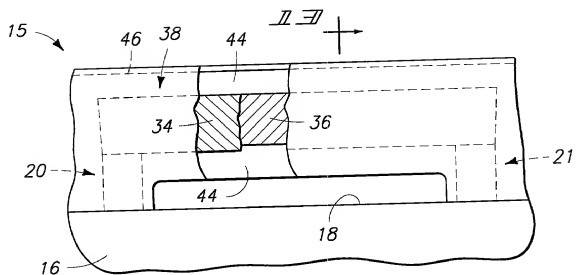
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